

CLAIM AMENDMENTS

1.-44. (Cancelled)

45. (Previously Presented) A method comprising:
providing a light modulator comprising an array of pixel cells and memory buffers, each memory buffer being associated with a different group of two or more of the pixel cells and each memory buffer being located closer to the associated group of pixel cells than another one of the group of pixel cells; and
providing a refresh signal;
in response to the refresh signal, reading digital indications stored in the memory buffers, converting the digital indications into analog voltages and updating charge intensities on the pixel cells using the analog voltages.

46. (Previously Presented) The method of claim 45, wherein the memory buffers are localized to the different groups.

47. (Previously Presented) The method of claim 45, wherein the memory buffers comprise a static random access memories.

48. (Cancelled)

49. (Previously Presented) The method of claim 45, further comprising:
during the refresh operation, latching the digital indications.

50. (Previously Presented) A light modulator comprising:
an array of pixel cells;
memory buffers being spatially distributed among the pixel cells, each memory buffer being associated with a different group of two or more of the pixel cells and storing a digital indications of associated predetermined voltages;
digital-to-analog converters to convert the digital indications into analog voltages to update charges on the pixel cells; and
sense amplifiers to respond to a refresh signal, to read the digital indications from the memory and provide the digital indications to the digital-to-analog converters.

51. (Previously Presented) The light modulator of claim 50, wherein the refresh operation occurs at a different rate than a frame update operation to the pixel cells.

52. (Previously Presented) The light modulator of claim 50, wherein at least one of the memory buffers comprise static random access memory.

53. (New) The method of claim 45, further comprising:
in response to a change in a state of the refresh signal, reading the digital indications stored in the memory buffers, converting the digital indications into the analog voltages and updating the charge intensities.

54. (New) The method of claim 45, further comprising:
responding with a sense amplifier to the refresh signal to read the digital indications stored in the memory buffers.